



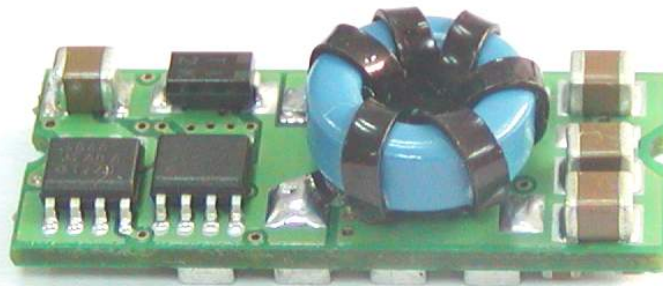
NON-ISOLATED DC-DC Converter

**S10 SERIES
3.0-5.5Vin, 1-3.3Vout, 10A**

**APPLICATION NOTES
Ver 1.0**



S10-5SX.XT (Through-Hole) Series



S10-5SX.X SMT Series

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1. Introduction

This application note describes the features and functions of Intronics' S10 SMT series of Non Isolated DC-DC Converters.

These are highly efficient, reliable and compact, high power density, single output DC/DC converters. These "Point of Load" modules serve the needs specifically of the fixed and mobile telecommunications and computing market, employing economical distributed Power Architectures.

The S10 SMT series provide precisely regulated output voltage range from 1.0V to 3.3Vdc over a wide range of input voltage ($V_i=3.0 - 5.5Vdc$) and can operate over an ambient temperature range of $-40C$ to $+85C$. Ultra-high efficiency operation is achieved through the use of synchronous rectification and drive control techniques. The modules are fully protected against short circuit and over-temperature conditions.

Intronics' world class automated manufacturing methods, together with an extensive testing and qualification program, ensure that all S10-5SX.X series converters are extremely reliable.

2. Models

The adjustable S10 series currently comprises of 7 models.

Model	Input Voltage	Output Voltage	Output Current
S10-5S1.0T	3.0 – 5.5VDC	1.0V	10A
S10-5S1.2T	3.0 – 5.5VDC	1.2V	10A
S10-5S1.5T	3.0 – 5.5VDC	1.5V	10A
S10-5S1.8T	3.0 – 5.5VDC	1.8V	10A
S10-5S2.5T	3.0 – 5.5VDC	2.0V	10A
S10-5S3.3T	3.0 – 5.5VDC	2.5V	10A
S10-5S5.0T	4.5 – 5.5VDC	3.3V	10A

Table 1A – S10-5SX.XT Through-Hole Series Models

The adjustable SMT10-05 series comprises of 7 models.

Model	Input Voltage	Output Voltage	Output Current
S10-5S1.0	3.0 – 5.5VDC	1.0V	10A
S10-5S1.2	3.0 – 5.5VDC	1.2V	10A
S10-5S1.5	3.0 – 5.5VDC	1.5V	10A
S10-5S1.8	3.0 – 5.5VDC	1.8V	10A
S10-5S2.5	3.0 – 5.5VDC	2.0V	10A
S10-5S3.3	3.0 – 5.5VDC	2.5V	10A
S10-5S5.0	4.5 – 5.5VDC	3.3V	10A

Table 1B – S10-5SX.X SMT Series Models

3. 10A SIP/SMT Converter Features

- High efficiency topology, typically 95% at 3.3Vdc
- Industry standard footprint
- Wide ambient temperature range, $-40C$ to $+85C$
- Cost efficient open frame design
- $\pm 10\%$ output voltage trimmable.
- No minimum load requirement (Stable at all loads)
- Remote ON/OFF
- Remote sense compensation
- Fixed switching frequency
- Continuous short-circuit protection and over current protection
- Over-temperature protection (OTP)
- Monotonic Startup with pre-bias at the output.
- UL/IEC/EN60950 Certified.

4. General Description

4.1 Electrical Description

A block diagram of the S10-5SX.X Series converter is shown in Figure 1. Extremely high efficiency power conversion is achieved through the use of synchronous rectification and drive techniques.

Essentially, the powerful S10-5SX.X series topology is based on a non-isolated synchronous buck converter. The control loop is optimized for unconditional stability, fast transient response and a very tight line and load regulation. In a typical pre-bias application the S10-5SX.X series converters do not draw any reverse current at start-up. The output is adjustable over a range of -10% to $+10\%$ of the nominal output voltage, using the TRIM pin.

The converter can be shut down via a remote ON/OFF input that is referenced to ground. This input is compatible with popular logic devices; a 'negative' logic input is supplied as standard. Negative logic implies that the converter is enabled if the remote ON/OFF input is low (or floating), and disabled if it is high.

The converter is also protected against over-temperature conditions. If the converter is overloaded or the ambient temperature gets too high, the converter will shut down to protect the unit.

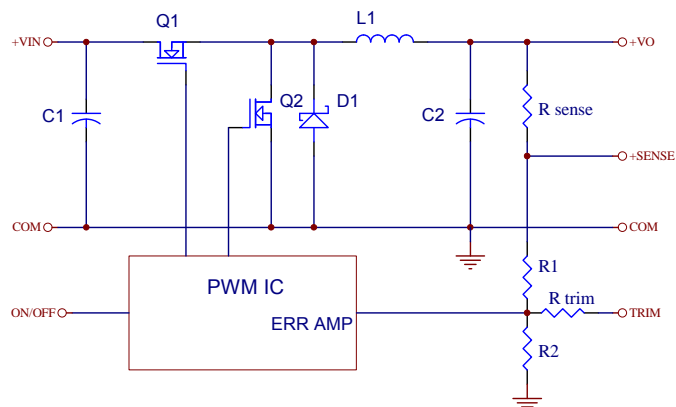


Figure 1. Electrical Block Diagram

4.2 Thermal Packaging and Physical Design.

The S10-5SX.X series uses a multi-layer FR4 PCB construction. All surface mount power components are placed on one side of the PCB, and all low-power control components are placed on the other side. Thus, the Heat dissipation of the power components is optimized, ensuring that control components are not thermally stressed. The converter is an open-frame product and has no case or case pin. The open-frame design has several advantages over encapsulated closed devices. Among these advantages are:

- **Efficient Thermal Management:** the heat is removed from the heat generating components without heating more sensitive, small signal control components.
- **Environmental:** Lead free open-frame converters are more easily re-cycled.
- **Cost Efficient:** No encapsulation. Cost efficient open-frame construction.
- **Reliable:** Efficient cooling provided by open frame construction offers high reliability and easy diagnostics.

5. Main Features and Functions

5.1 Operating Temperature Range

Intronics' S10-5SX.X series converters highly efficient converter design has resulted in its ability to operate over a wide ambient temperature environment (-40C to 85C). Due consideration must be given to the de-rating curves when ascertaining maximum power that can be drawn from the converter. The maximum power drawn is influenced by a number of factors, such as:

- Input voltage range.
- Output load current.
- Air velocity (forced or natural convection).
- Mounting orientation of converter PCB with respect to the Airflow.
- Motherboard PCB design, especially ground and power planes. These can be effective heatsinks for the converter.

5.2 Over-Temperature Protection (OTP)

The S10-5SX.X Series converters are equipped with non-latching over-temperature protection. A temperature sensor monitors the temperature of the hot spot (typically, top switch). If the temperature exceeds a threshold of 120°C (typical) the converter will shut down, disabling the output. When the temperature has decreased the converter will automatically restart.

The over-temperature condition can be induced by a variety of reasons such as external overload condition or a system fan failure.

5.3 Output Voltage Adjustment

Section 7.8 describes in detail as to how to trim the output voltage with respect to its set point. The output voltage on all models is trimmable in the range -10% to +10%.

5.4 Safe Operating Area (SOA)

Figure 2 provides a graphical representation of the Safe Operating Area (SOA) of the converter. This representation assumes ambient operating conditions such as airflow are met as per thermal guidelines provided in Sections 7.2 and 7.3.

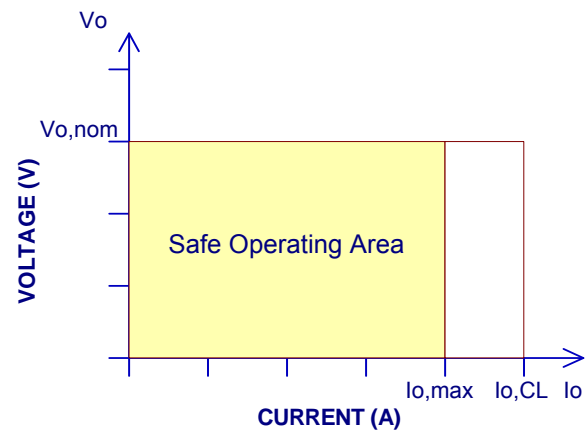


Figure 2. Maximum Output Current Safe Operating Area

5.5 Over Current Protection

All different voltage models have a full continuous short-circuit protection. The unit will auto recover once the short circuit is removed. To provide protection in a fault condition, the unit is equipped with internal over-current protection. The unit operates normally once the fault condition is removed. The power module will supply up to 170% of rated current. In the event of an over current converter will go into a hiccup mode protection.

5.6 Remote ON/OFF

The remote ON/OFF input feature of the converter allows external circuitry to turn the converter ON or OFF. Active-low remote ON/OFF is available as standard.

The S10-5SX.X series converters are turned on if the remote ON/OFF pin is low, or left open or floating. Pulling the pin high will turn the converter 'Off'. The signal level of the remote on/off input is defined with respect to ground. The unit is guaranteed OFF over the full temperature range if this voltage level exceeds 2.8Vdc.

The remote ON/OFF input can be driven as described in Figure 3.

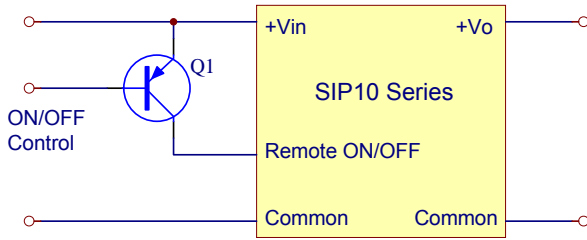
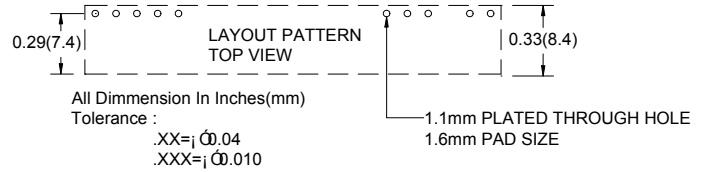


Figure 3 . Remote ON/OFF Input Drive Circuit

given to proper low impedance tracks between power module, input and output grounds.



VIEW IS FROM TOP SIDE
 Figure 4A. Recommended SIP Footprint

5.7 UVLO (Under-Voltage Lockout)

The voltage on the Vcc pin determines the start of the operation of the Converter. When the input Vcc rises and exceeds about 2.8V the converter initiates a soft start. The UVLO function in the converter has a Hysterisis (about 100mV) built in to provide noise immunity at start-up.

6. Safety

6.1 Input Fusing and Safety Considerations.

Agency Approvals: The power Supply shall be submitted to and receive formal approval from the following test agencies.

1. The power supply shall be approved by a nationally recognized testing laboratory to UL/CSA 60950 3rd Edition (North America) and EN60950 (International)

2. CB Certificate from an internationally recognized test house in accordance with EN 60950.

The S10-5SX.X series converters do not have an internal fuse. However, to achieve maximum safety and system protection, always use an input line fuse. The safety agencies require a time-delay fuse with a maximum rating of 20A.

7. Applications

7.1 Layout Design Challenges.

In optimizing thermal design the PCB is utilized as a heatsink. Also some heat is transferred from the SIP module to the main board through connecting pins. The system designer or the end user must ensure that other components and metal in the vicinity of the S10-5SX.X series SIP's meet the spacing requirements to which the system is approved.

Low resistance and low inductance PCB layout traces are the norm and should be used where possible. Due consideration must also be

Recommended Pad Layout

Dimensions are in millimetres and(inches)

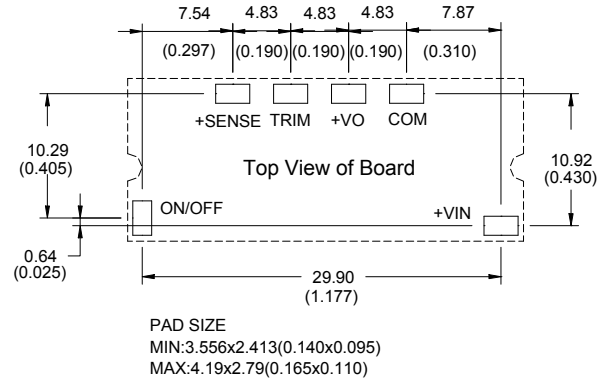


Figure 4B. Recommended SMT Footprint

7.2 Convection Requirements for Cooling

To predict the approximate cooling needed for the module, refer to the Power De-rating curves in Figures 9 and 10. These de-rating curves are approximations of the ambient temperatures and airflows required to keep the power module temperature below its maximum rating. Once the module is assembled in the actual system, the module's temperature should be checked as shown in Figure 5 to ensure it does not exceed 110°C.

Proper cooling can be verified by measuring the power module's temperature at Q1-pin 6 and Q2-pin 6 as shown in Figure 6A,6B.

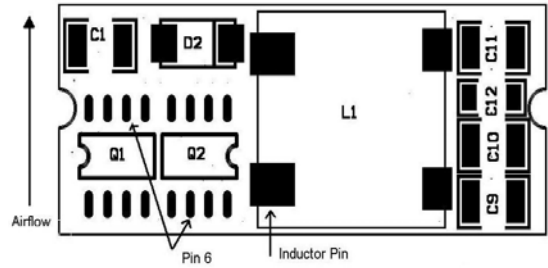


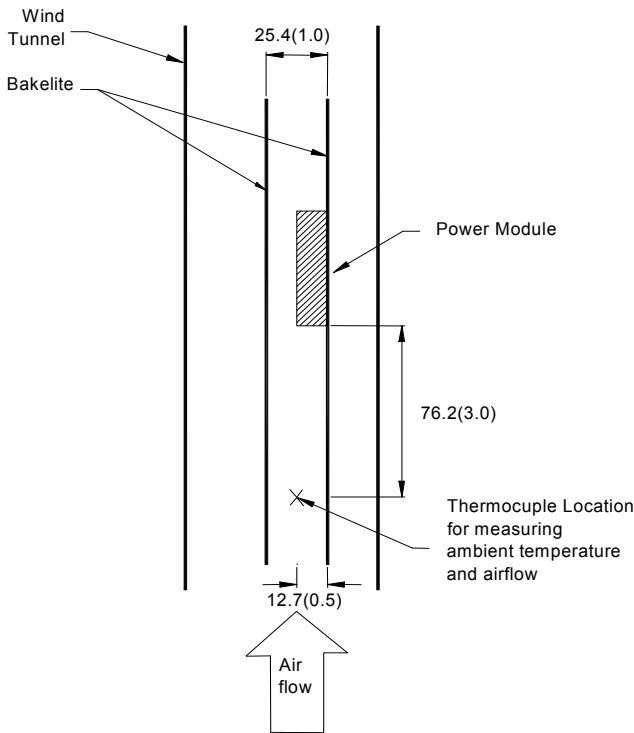
Figure 6B. Temperature Measurement Location for SMT

7.3 Thermal Considerations

The power module operates in a variety of thermal environments; however, sufficient cooling should be provided to help ensure reliable operation of the unit. Heat is removed by conduction, convection, and radiation to the surrounding environment. The thermal data presented is based on measurements taken in a set-up as shown in Figure 5. Figures 7A,7B and 8A,8B represent the test data. Note that the airflow is parallel to the long axis of the module as shown in Figure 6A for the SIP.

The temperature at either location should not exceed 110 °C. The output power of the module should not exceed the rated power for the module (VO, set x IO, max).

The SMT10 thermal data presented is based on measurements taken in a wind tunnel. The test setup shown in Figure 5 and EUT need to solder on 33mm x 40.38mm(1.300" x 1.59") test pcb. Note that airflow is parallel to the long axis of the module as shown in Fig 6B



Note : Dimensions are in millimeters and (inches)

Figure 5. Thermal Test Setup

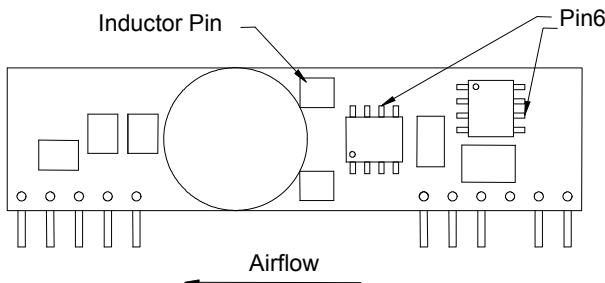


Figure 6A. Temperature Measurement Location for SIP

7.4 Power De-Rating Curves

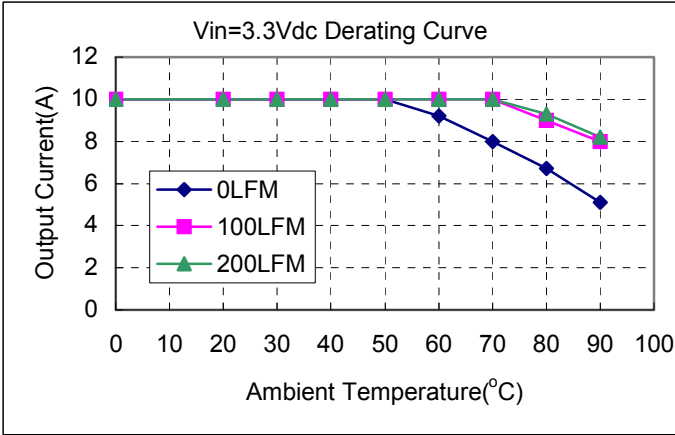


Figure7A Typical Power De-rating for 3.3V IN(SIP10)

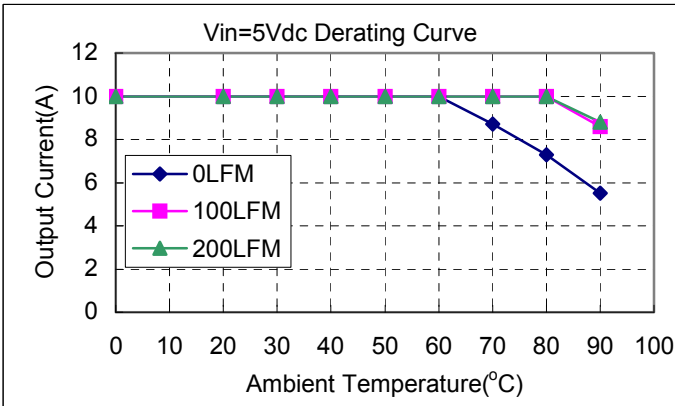


Figure7B.Typical Power De-rating for 5.0V IN(SIP10)

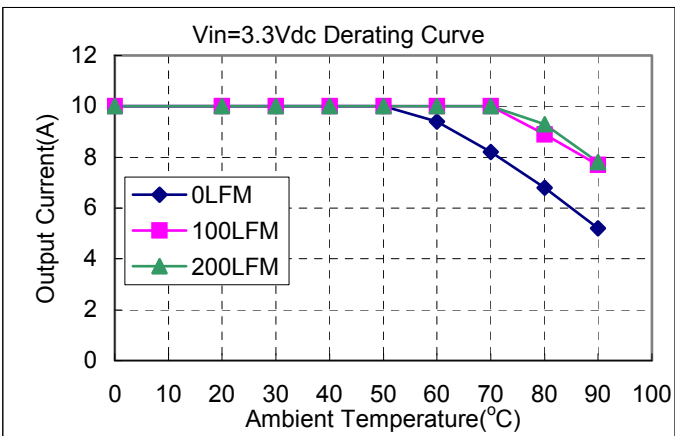


Figure8A.Typical Power De-rating for 3.3V IN (SMT10)

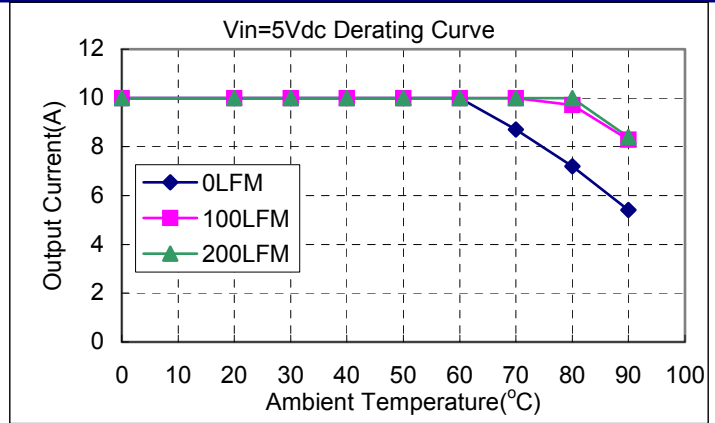


Figure8B.Typical Power De-rating for 5.0V IN(SMT10)

7.5 Input Capacitance at the Power Module

The SIP/SMT converters must be connected to a low AC source impedance. To avoid problems with loop stability source inductance should be low. Also, the input capacitors should be placed close to the converter input pins to de-couple distribution inductance. However, the external input capacitors are chosen for suitable ripple handling capability. Low ESR polymers are a good choice. They have high capacitance, high ripple rating and low ESR (typical <math><20\text{m}\Omega</math>). Electrolytic capacitors should be avoided. Circuit as shown in Figure 9 represents typical measurement methods for ripple current. Input reflected-ripple current is measured with a simulated source Inductance of 1uH. Current is measured at the input of the module.

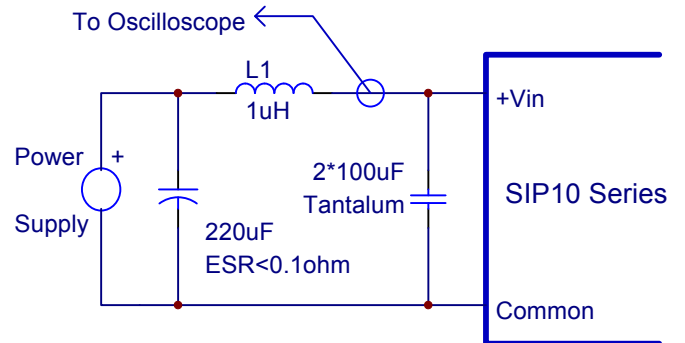


Figure 9. Input Reflected-Ripple Test Setup

7.6 Test Set-Up

The basic test set-up to measure parameters such as efficiency and load regulation is shown in Figure 10. Things to note are that this converter is non-isolated, as such the input and output share a common ground. These grounds should be connected together via low impedance ground plane in the application circuit. When testing a converter on a bench set-up, ensure that -Vin and -Vo are connected together via a low impedance short to ensure proper efficiency and load regulation measurements are being made. When testing the Intronics' S10-5SX.X series under any transient conditions please ensure that the transient response of the source is sufficient to power the equipment under test. We can calculate the

- Efficiency
- Load regulation and line regulation.

The value of efficiency is defined as :

$$\eta = \frac{V_o \times I_o}{V_{in} \times I_{in}} \times 100\%$$

Where: Vo is output voltage ,
 Io is output current,
 Vin is input voltage,
 Iin is input current.

The value of load regulation is defined as :

$$Load.reg = \frac{V_{FL} - V_{NL}}{V_{NL}} \times 100\%$$

Where: V_{FL} is the output voltage at full load
 V_{NL} is the output voltage at no load

The value of line regulation is defined as:

$$Line.reg = \frac{V_{HL} - V_{LL}}{V_{LL}} \times 100\%$$

Where: V_{HL} is the output voltage of maximum input voltage at full load.
 V_{LL} is the output voltage of minimum input voltage at full load.

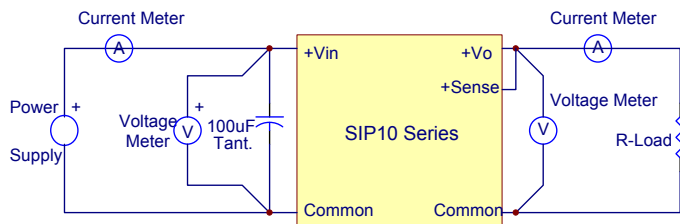


Figure 10. SIP10 Series Test Setup

7.7 Remote Sense Compensation

Remote Sense regulates the output voltage at the point of load. It minimizes the effects of distribution losses such as drops across the connecting pin and PCB tracks (see Figure 11). Please note however, the maximum drop from the output pin to the point of load should not exceed 500mV for remote compensation to work.

The amount of power delivered by the module is defined as the output voltage multiplied by the output current (VO x IO).

When using TRIM UP, the output voltage of the module will increase which, if the same output current is maintained, increases the power output by the module. Make sure that the maximum output power of the module remains at or below the maximum rated power.

When the Remote Sense feature is not being used, leave sense pin disconnected.

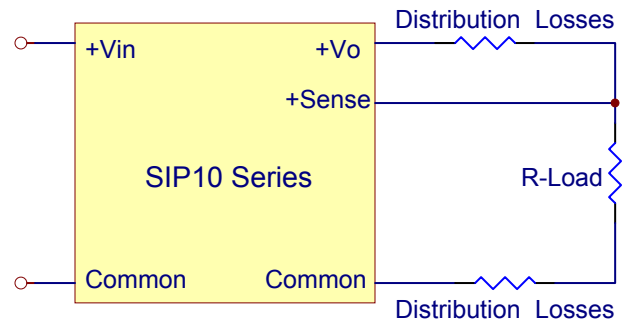


Figure 11. Circuit Configuration for Remote Sense Operation

7.8 S10-5SX.X Series Output Voltage Adjustment.

In order to trim the voltage up or down one needs to connect the trim resistor either between the trim pin and ground for trim-up and between trim pin and V_{sense+} for trim-down. The output voltage trim range is ±10%. This is shown in Figures 12 and 13:

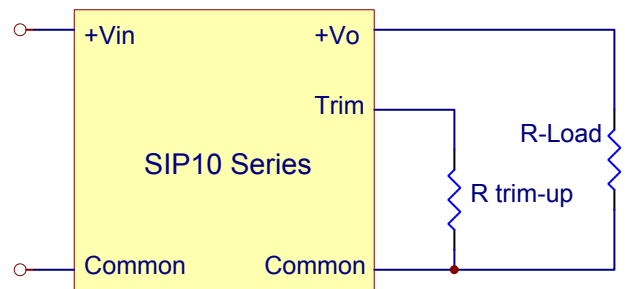


Figure 12. Trim-up Voltage Setup

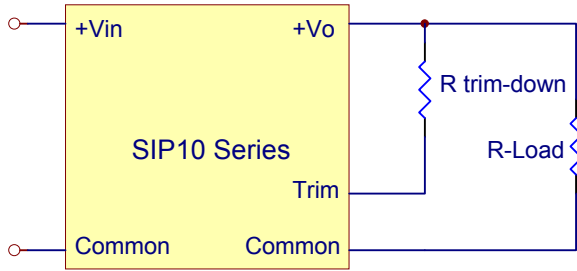


Figure 13. Trim-down Voltage Setup

Where: $R_{trim-down}$ is the external resistor in $K\Omega$.
 $V_{o,nom}$ is the nominal output voltage.
 V_o is the desired output voltage.
 R_1 and R_t are internal to the unit and are defined in Table 2.

For example, to trim-down the output voltage of 2.5V module (SIP10-05S25E) by 8% to 2.3V, $R_{trim-down}$ is calculated as follows :

$$\begin{aligned} V_{o,nom} - V_o &= 2.5 - 2.3 = 0.2 \text{ V} \\ R_1 &= 30.1 \text{ K}\Omega \\ R_t &= 78.7 \text{ K}\Omega \end{aligned}$$

$$R_{trim-down} = \frac{30.1 \times (2.3 - 0.8)}{0.2} - 78.7 = 147.05 \text{ (K}\Omega\text{)}$$

For Trim-up using an external voltage source, apply a voltage from TRIM pin to ground using the following equation:

$$V_{trim-up} = 0.8 - \left(\frac{(V_o - V_{o,nom}) \times R_t}{R_1} \right)$$

For Trim-down using an external voltage source, apply a voltage from TRIM pin to ground using the following equation :

$$V_{trim-down} = 0.8 + \frac{(V_{o,nom} - V_o) \times R_t}{R_1}$$

Where: $V_{trim-up}$ is the external source voltage for trim-up.
 $V_{trim-down}$ is the external source voltage for trim-down.
 V_o is the desired output voltage.
 $V_{o,nom}$ is the nominal output voltage.
 R_t (internal to the module) is defined in Table 2.

If the TRIM feature is not being used, leave the TRIM pin disconnected.

The value of $R_{trim-up}$ defined as:

$$R_{trim-up} = \left(\frac{R_1 \times 0.8}{V_o - V_{o,nom}} \right) - R_t \text{ (K}\Omega\text{)}$$

Where: $R_{trim-up}$ is the external resistor in $K\Omega$.
 $V_{o,nom}$ is the nominal output voltage.
 V_o is the desired output voltage.
 R_1 and R_t are internal to the unit and are defined in Table 2.

Output Voltage(V)	R1 (KΩ)	Rt (KΩ)
1.0	30.1	30.1
1.2	30.1	59
1.5	30.1	100
1.8	30.1	100
2.0	30.1	100
2.5	30.1	78.7
3.3	30.1	59

Table 2 – Trim Resistor Values

For example, to trim-up the output voltage of 1.5V module (SIP10-05S15E) by 8% to 1.62V, $R_{trim-up}$ is calculated as follows :

$$\begin{aligned} V_o - V_{o,nom} &= 1.62 - 1.5 = 0.12 \text{ V} \\ R_t &= 100 \text{ K}\Omega \\ R_1 &= 30.1 \text{ K}\Omega \end{aligned}$$

$$R_{trim-up} = \frac{30.1 \times 0.8}{0.12} - 100 = 100.66 \text{ (K}\Omega\text{)}$$

The value of $R_{trim-down}$ defined as:

$$R_{trim-down} = \frac{R_1 \times (V_o - 0.8)}{V_{o,nom} - V_o} - R_t \text{ (K}\Omega\text{)}$$

- Servers, Switches and Data Storage
- Networking Gear
- Wireless Communications
- Data Communications
- Distributed Power Architecture
- Telecommunications
- Semiconductor Test Equipment
- Industrial / Medical

7.9 Output Ripple and Noise Measurement

The test set-up for noise and ripple measurements is shown in Figure 14. a 50Ω coaxial cable with a 50Ω termination was used to prevent impedance mismatch reflections disturbing the noise readings at higher frequencies.

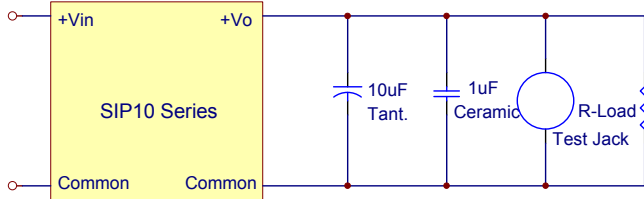


Figure 14. Output Voltage Ripple and Noise Measurement Set-Up

7.10 Output Capacitance

Intronics' S10-5SX.X series converters provide unconditional stability with or without external capacitors. For good transient response low ESR output capacitors should be located close to the point of load. For high current applications point has already been made in layout considerations for low resistance and low inductance tracks.

Output capacitors with its associated ESR values have an impact on loop stability and bandwidth. Intronics' converters are designed to work with load capacitance up-to 10,000uF. It is recommended that any additional capacitance, typically 1,000uF and low ESR (<20mΩ), be connected close to the point of load and outside the remote compensation point.

7.11 SMT Reflow Profile

An example of the SMT reflow profile is given in Figure 15.

Equipment used: SMD HOT AIR REFLOW HD-350SAR

Alloy: AMQ-M293TA or NC-SMQ92 IND-82088 SN63

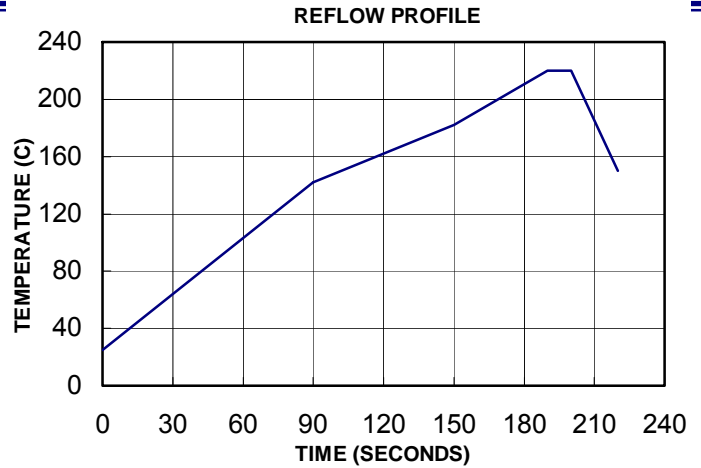


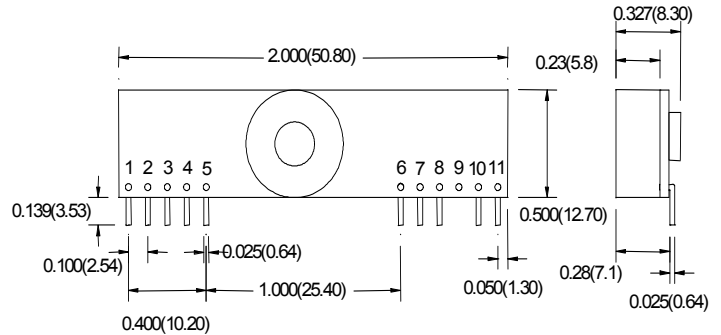
Figure 15 – SMT Reflow Profile

8. Mechanical Outline Diagrams

8.1 SIP/SMT10 Mechanical Outline Diagrams

Dimensions are in millimeters and (inches)

Tolerance : x.xx ±0.02 in.(0.5mm) , x.xxx ±0.010 in. (0.25 mm) unless otherwise noted

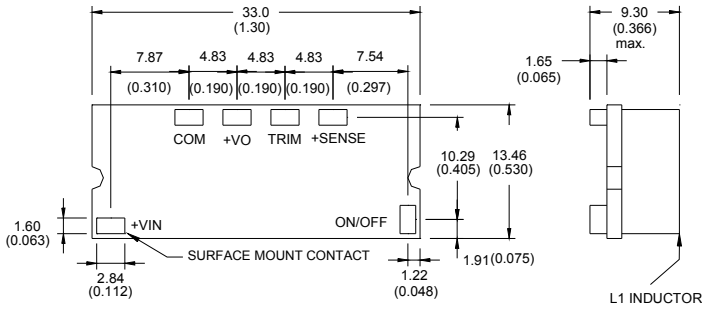


PIN CONNECTION	
Pin	FUNCTION
1	+Output
2	+Output
3	+Sense
4	+Output
5	Common
6	Common
7	+V Input
8	+V Input
9	No Pin
10	Trim
11	On/Off Control

Figure 16. SIP10 Mechanical Outline Diagram

- Servers, Switches and Data Storage
- Wireless Communications
- Distributed Power Architecture
- Semiconductor Test Equipment
- Networking Gear
- Data Communications
- Telecommunications
- Industrial / Medical

BOTTOM VIEW OF BOARD

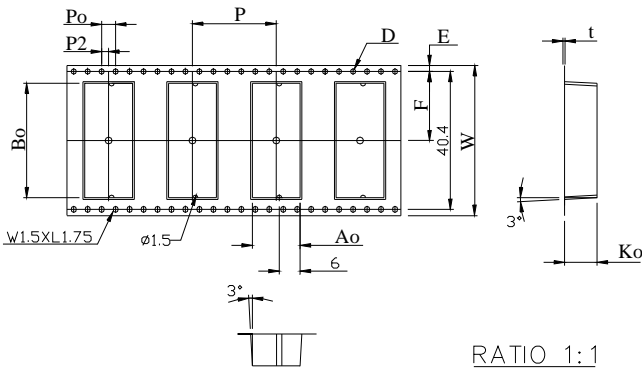


Dimensions are in millimeters(Inches)
 Tolerances :X.X; \varnothing .5mm(0.02in),X.XX; \varnothing .25mm(0.010in),unless otherwise noted.

Figure 17. SMT10 Mechanical Outline Diagram

8.2 SMT Tape and Reel Dimensions

The Tape Reel dimensions for the SMT module is shown in Figure 18.



ITEM	SPEC
W	44.00 ^{+0.30} _{-0.30}
Ao	13.70 ^{+0.10} _{-0.10}
Bo	33.50 ^{+0.10} _{-0.10}
Ko	9.30 ^{+0.10} _{-0.10}
P	24.00 ^{+0.10} _{-0.10}
F	20.20 ^{+0.10} _{-0.10}
E	1.75 ^{+0.10} _{-0.10}
D	1.50 ^{+0.10} _{-0.00}
D1	2.00 ^{+0.25} _{-0.00}
Po	4.00 ^{+0.10} _{-0.10}
P2	2.00 ^{+0.10} _{-0.10}
t	0.40 ^{+0.05} _{-0.05}

Figure 18 – SMT Tape and Reel Dimensions